(54) Title: ELECTRONIC COMPUTER SYSTEM AND PROCESSOR ELEMENTS USED FOR THIS SYSTEM

(54) 発明の名称　　　電子計算機システム及びこのシステムに使用されるプロセッサ要素
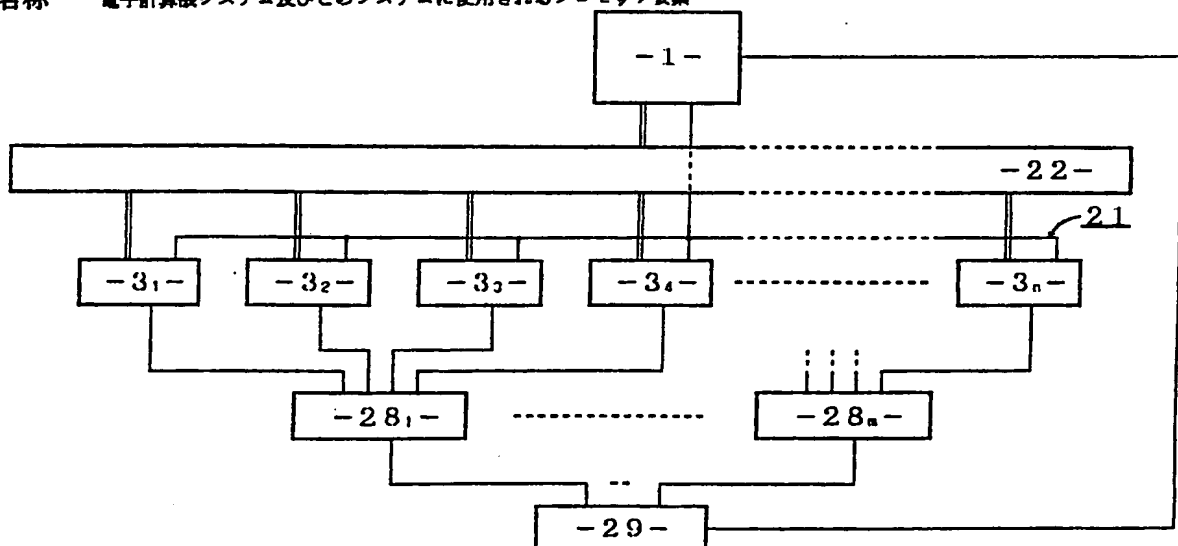
(57) Abstract

An electronic computer system comprising a control processor (1) and a plurality of processor elements (3_1-3_n) that transfer data to one another via a packet transmission network (22). Each of the processor elements $(3_1$ to $3_n)$ has a control unit (30i) that executes the flow of instructions sent from the control processor (1) based on the principle of SIMD system, and an execution unit (31i) that executes operation based on the data drive and executes the transfer of data among the processor elements (3_1-3_n) via the packet transmission network (22). The control processor (1) sends instruction to the control unit (30i) of each of processor elements (3_1-3_n) to set the content that is to be executed by the execution unit (31i) based on the data drive. The execution unit (31i) operates based on the data drive in parallel with the setting of the execution unit (31i).